

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	277	automat\$6 with generat\$3 with netlist	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/09 14:20
L2	13	automat\$6 with generat\$3 with netlist with pin	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/09 14:20
S1	1169	power adj pin and ground adj pin	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/10 16:36
S2	279065	layout	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/10 16:36
S3	332	S1 and S2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/10 16:36
S4	96408	pcb	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/09 14:19
S5	169	S3 and S4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/10 17:13
S6	14	programmable adj chip adj system	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/10 16:37

## EAST Search History

S7	0	S5 and S6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/10 16:37
S8	2809	(716/1,15-17).ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/10 17:15

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Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	(automat\$6 and netlist and layout and programmable and processor and core and synthesis and voltage and groun and pin and board and arbitrary and primary and secondary).clm.	US-PGPUB	OR	ON	2007/07/09 14:36
L2	0	(automat\$6 and netlist and layout and programmable and processor and core and synthesis and voltage and ground and pin and board and arbitrary and primary and secondary).clm.	US-PGPUB	OR	ON	2007/07/09 14:36